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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10020304	FILING DATE 12/13/2001	CLASS 438	SUBCLASS ---	GAU 2812	EXAMINER <i>Sefer</i>
**APPLICANTS: Kapre Ravindra; Hsiao Tommy; Wang Yanhua; Min Kyungjin;					
**CONTINUING DATA VERIFIED:					
BEST AVAILABLE COPY					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB DO NOT PUBLISH <input checked="" type="checkbox"/>			RESCIND <input type="checkbox"/>		
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			ATTORNEY DOCKET NO		
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no			01-721/LSI1P185		
Verified and Acknowledged Examiner's initials					
TITLE : Buried channel devices and a process for their fabrication simultaneously with surface channel devices to produce transistors and capacitors with multiple electrical gate oxides					
U.S. DEPT. OF COMMERCE / PAT. & TRADEMARK OFFICE (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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